	Туре	#	Hits	Search Text	DBs	Time Stamp Comments	Comments
_	BRS	7	96	(rack or cabinet or bay) and (shelves or drawers) and switch and (backplane or	US. PGPUB	US- PGPUB 2007/09/28 : 09:25	
				interconnect) and processor and stack and redundancy	USPAT		
N	BRS	L2	16	1 and (master near switch)	US- PGPUB	US- PGPUB 2007/09/28	
					USPAT	02:50	

	Туре	#	Hits	Search Text	DBs	Time Stamp Comments	Comments
-	BRS	7	86	shelves and (boards or modules or blades) and switch and (processors or computers) and (cabinet or rack) and (interconnect or backplane) and stack	US- PGPUB ; USPAT	US- PGPUB 2007/09/28 ; USPAT	
8	BRS	<b>L2</b>	73	1 and master	US- PGPUB ; USPAT	US- PGPUB 2007/09/28 ; 07:56 USPAT	
e	IS&R	<b>L</b> 3	8	(("7209358") or ("7032037")).PN.	US- PGPUB ; USPAT	US- PGPUB 2007/09/28 ; 07:57 USPAT	